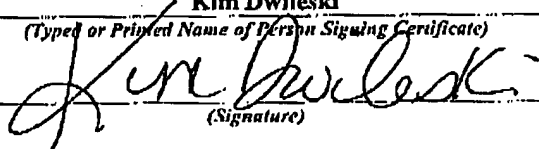


<b>CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)</b>			Docket No. <b>BUR920010100US1</b>
Applicant(s): <b>Joseph A. Iadanza</b>			
Application No. <b>09/683,872</b>	Filing Date <b>2/26/2002</b>	Examiner <b>Ortiz, Edgardo</b>	Group Art Unit <b>2815</b>
Invention: <b>METHOD OF CONNECTING CORE I/O PINS TO BACKSIDE CHIP I/O PADS</b>			
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Applicant: Joseph A. Iadanza

Group Art Unit: 2815

Filed: 02/26/2002

Examiner: Ortiz, Edgardo

Serial No.: 09/683,872

Docket No.: BUR920010100US1

Title: **METHOD OF CONNECTING CORE I/O PINS TO BACKSIDE CHIP I/O PADS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Request for Reconsideration**

Sir:

This Request for reconsideration is in response to the Final Office Action mailed

March 28, 2005.

09/683,872

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